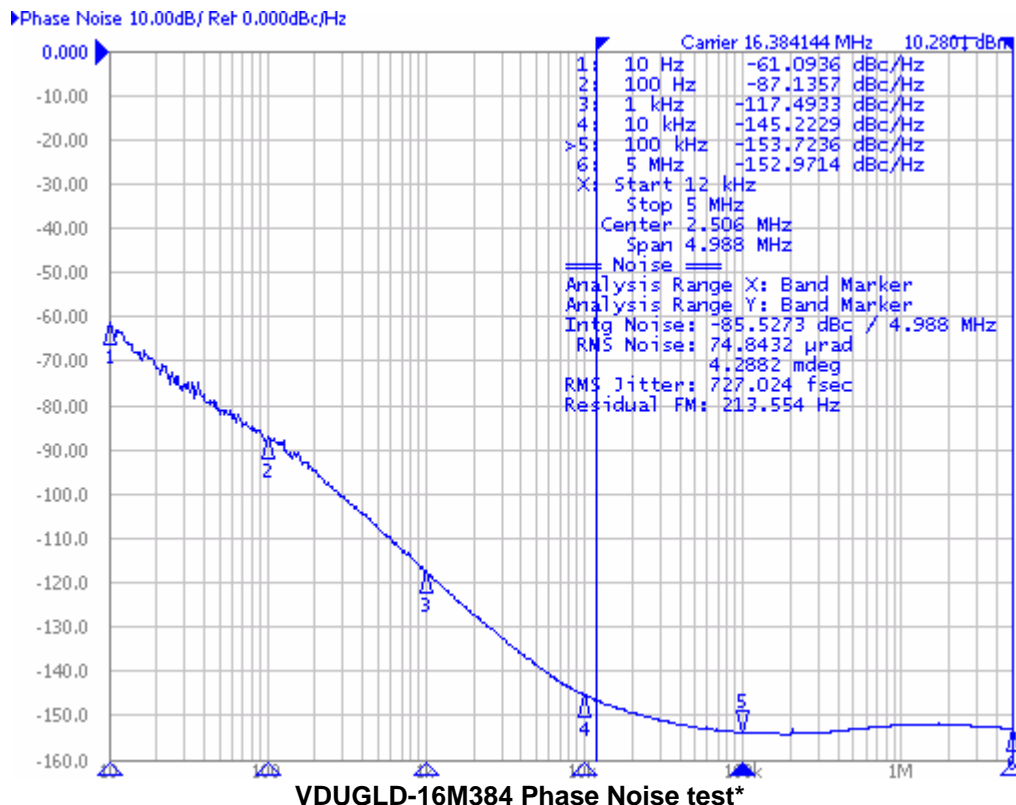
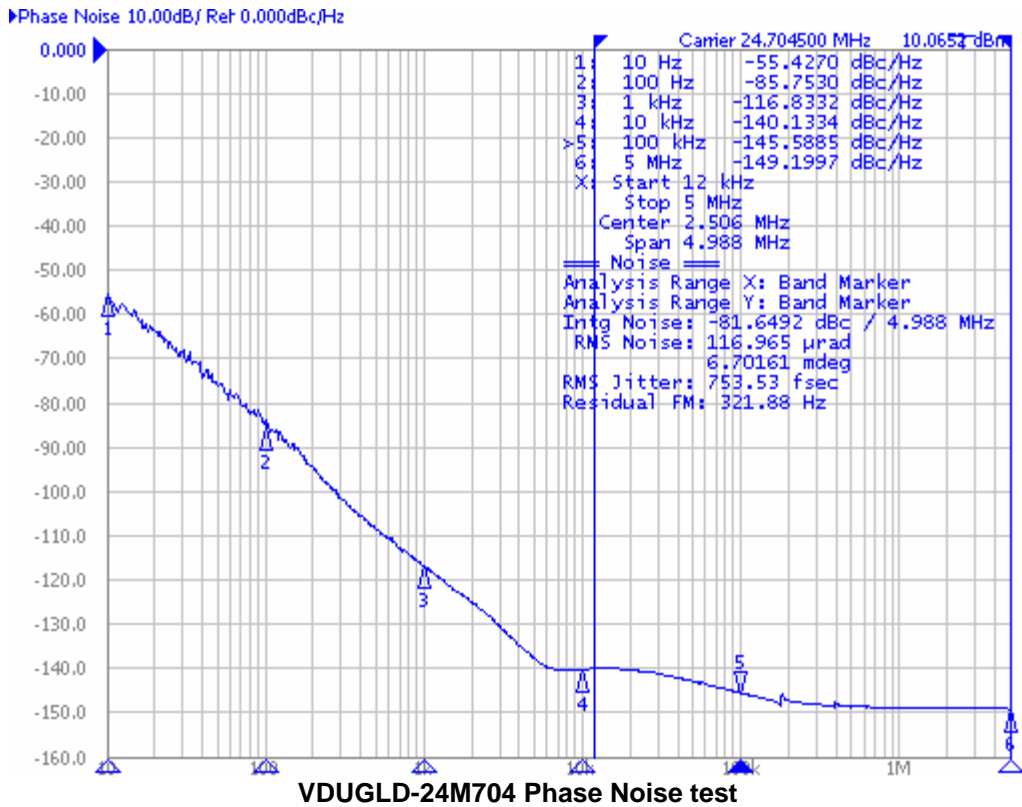
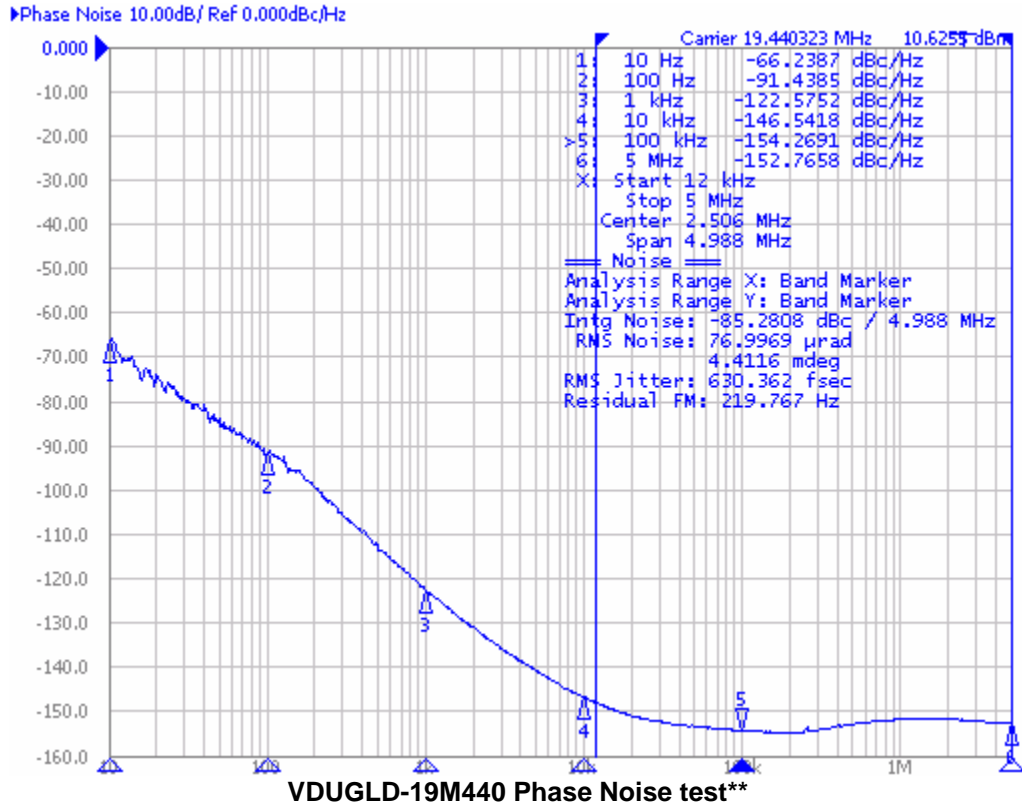


Phase Noise Results

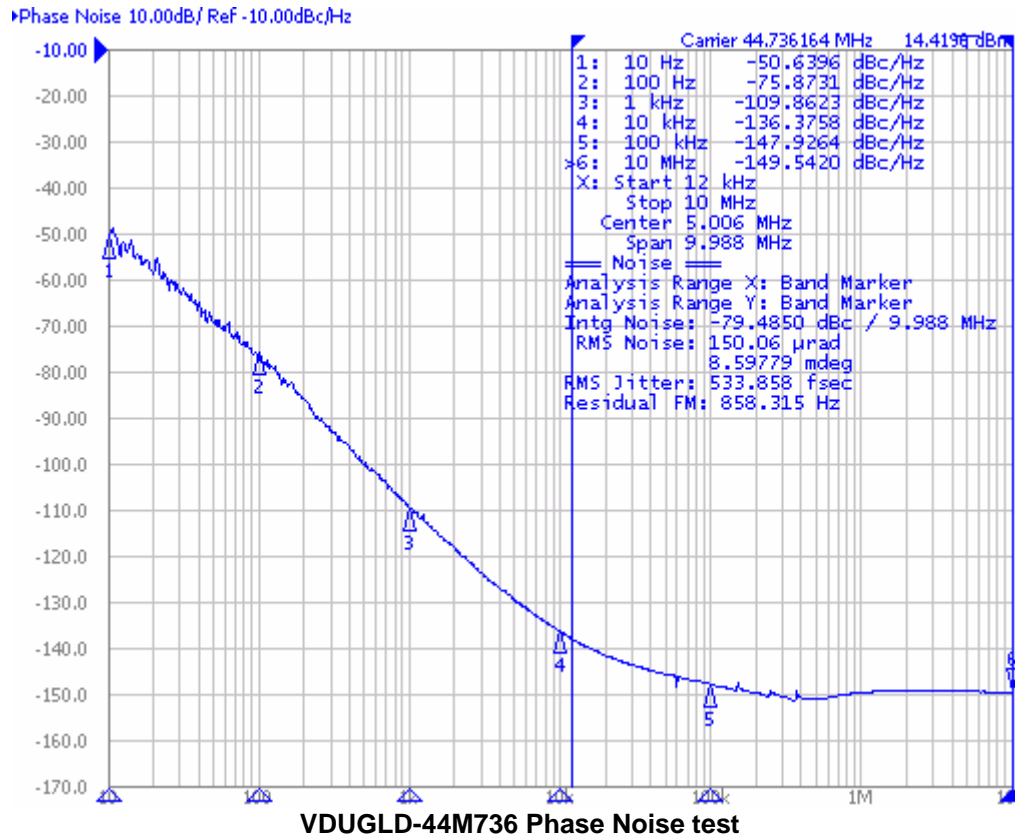
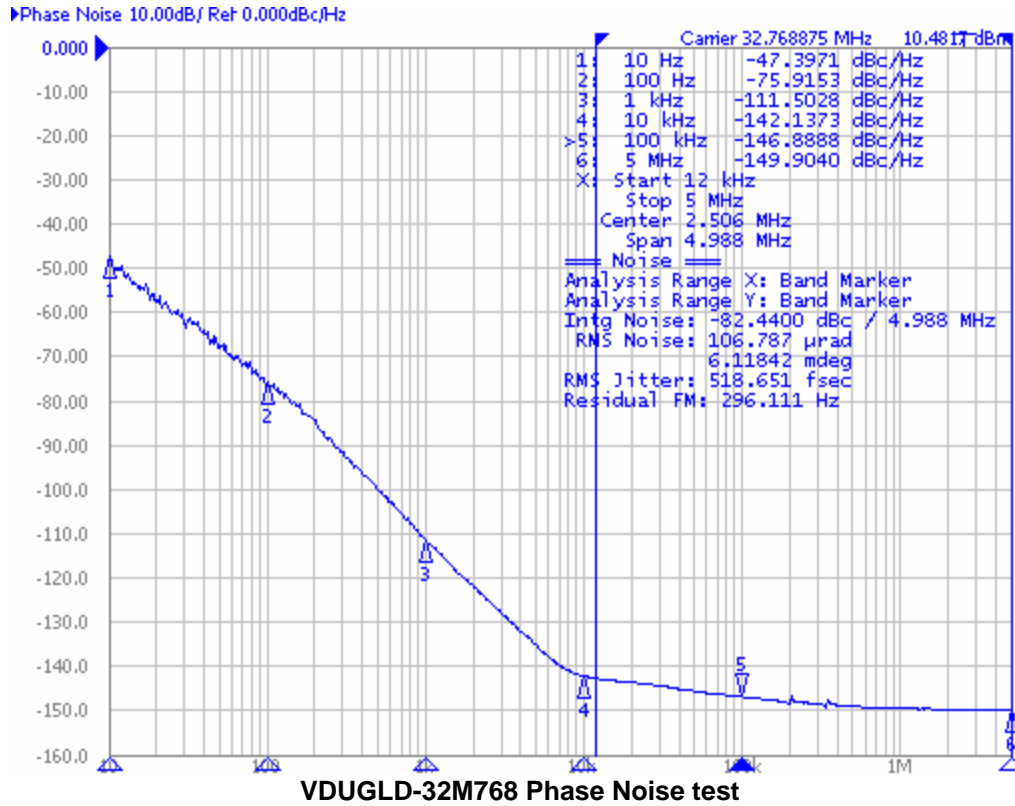
Phase noise measurements were performed on an Agilent E5052A signal source analyzer (SSA). The E5052A has a phase noise to jitter integration calculation feature and devices were characterized in the 12kHz-10MHz band (except for the lower frequencies where the equipment limitations prevented measurement to 10 MHz – see graphs for frequency band). Please contact Vectron for other offset integration bands.



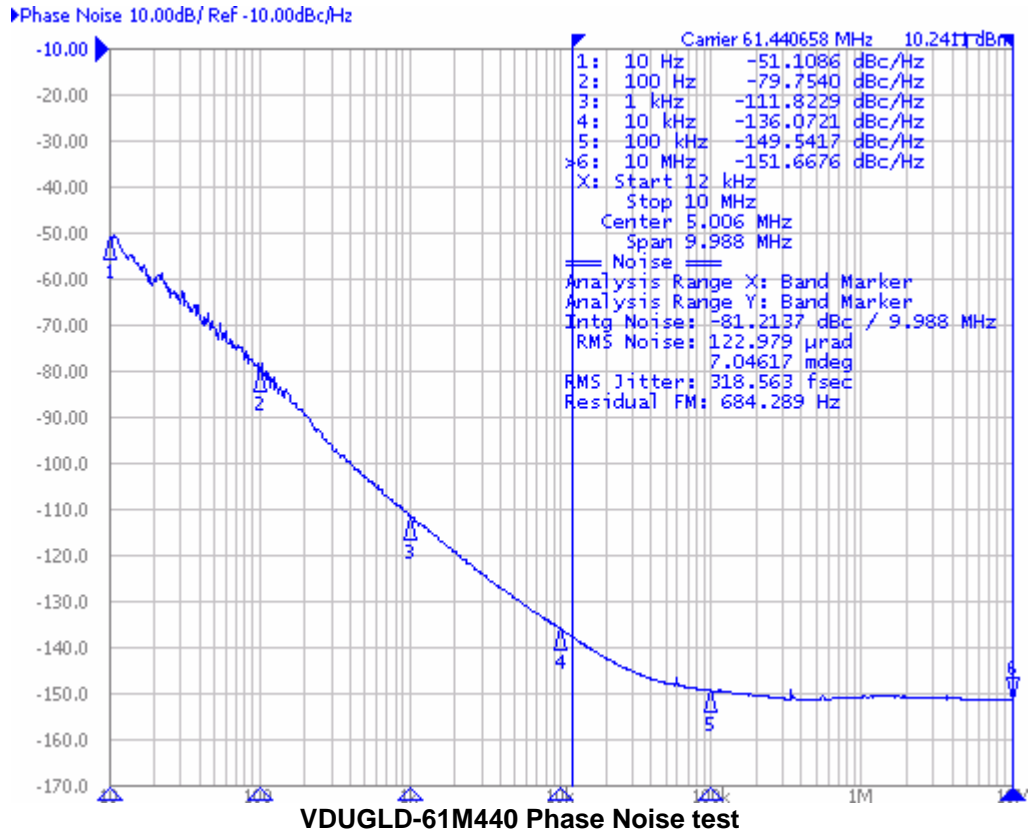
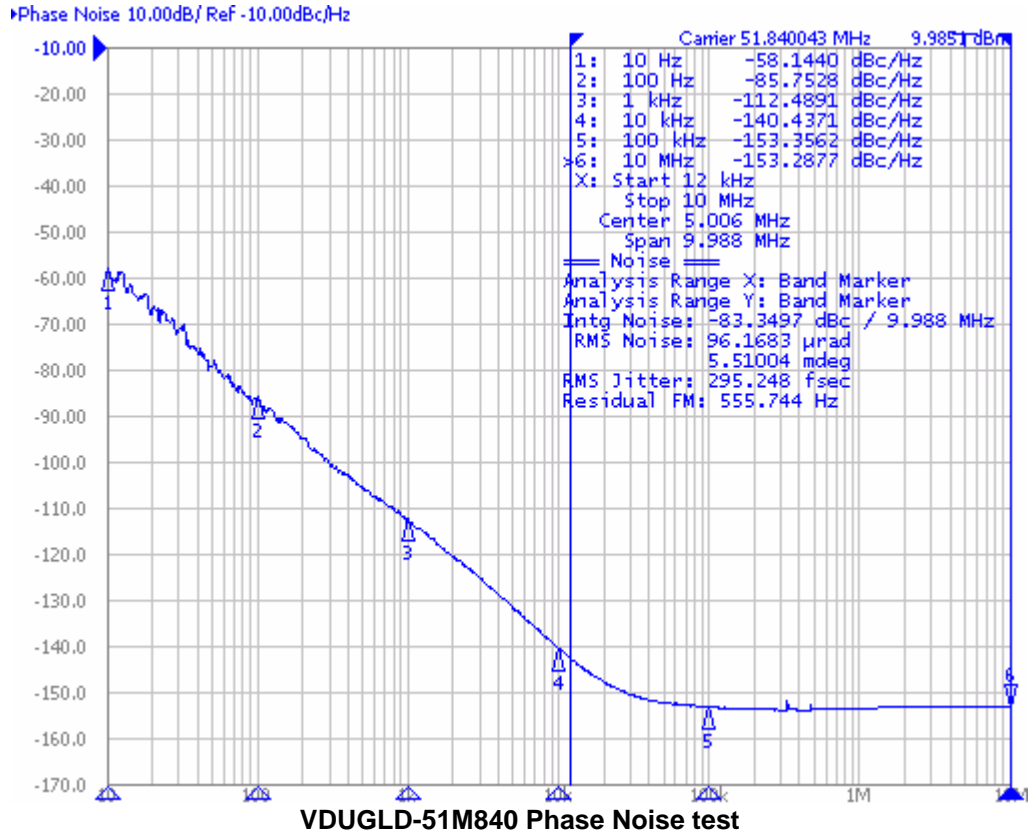
Typical Phase Noise for the V-Type Series



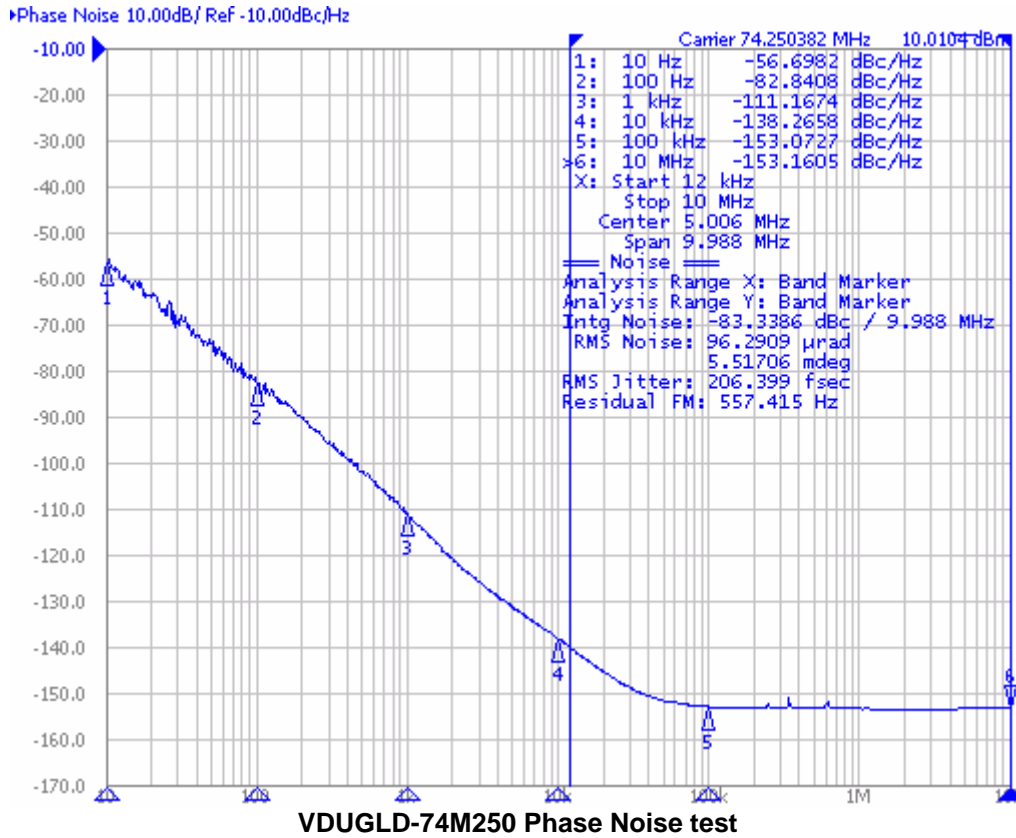
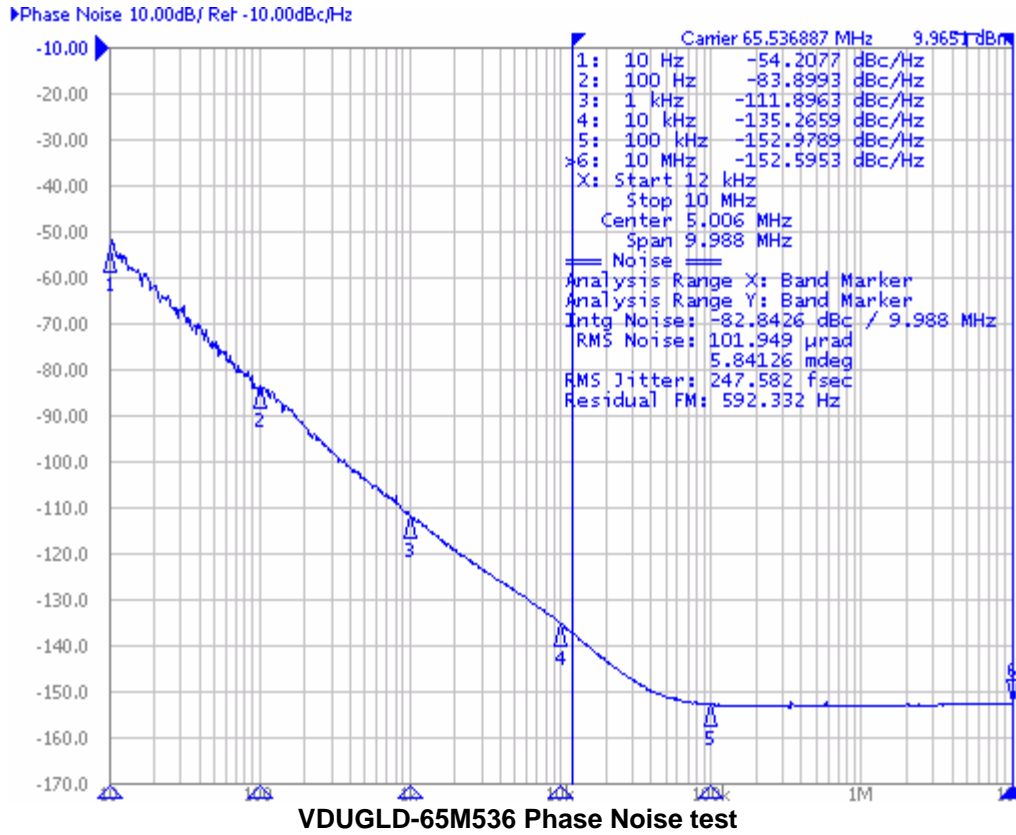
Typical Phase Noise for the V-Type Series



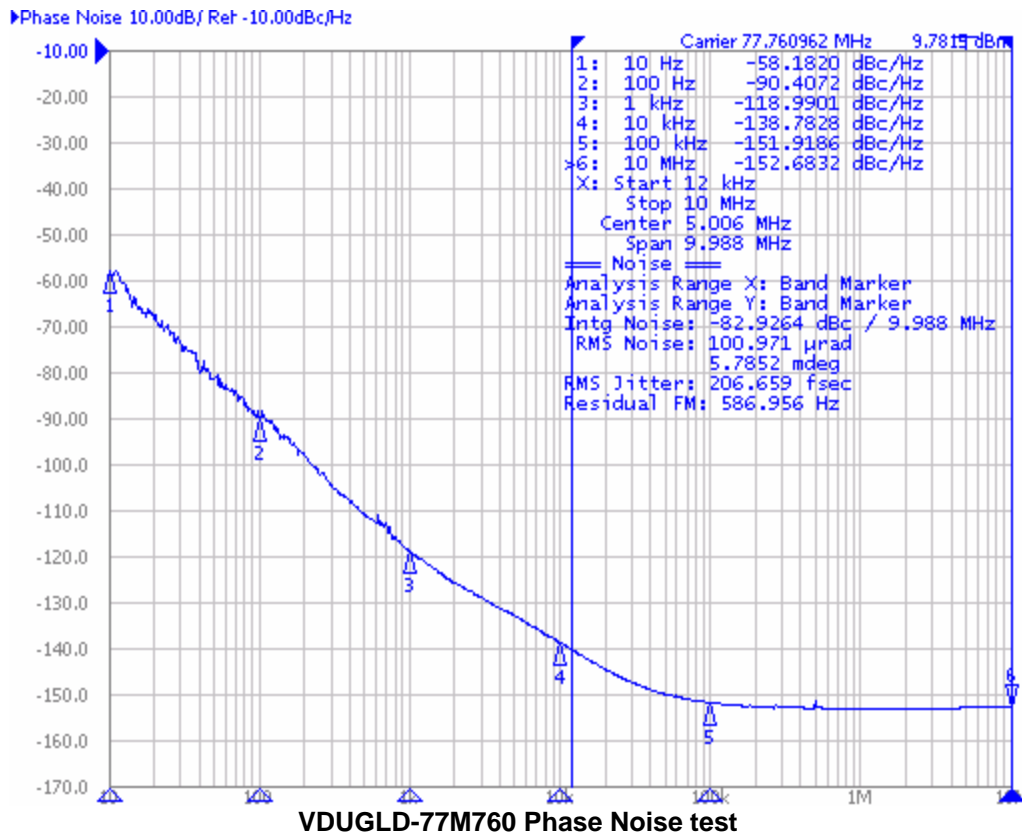
Typical Phase Noise for the V-Type Series



Typical Phase Noise for the V-Type Series



Typical Phase Noise for the V-Type Series



The majority of V-type products are built with a proprietary ASIC. This ASIC has different internal gain settings that allow the ASIC to be used over a wide range of frequencies. Due to these different gain settings, the close-in phase noise varies by frequency. Because of this, the variation does not follow the typical correlation of degraded phase noise and increased frequency. Several standard frequencies are shown in the above graphs, but typical data for most frequencies can be provided upon request.

Additionally, the graphs for the lower frequencies (16.384 MHz and 19.440 MHz) are manufactured with optimal phase noise. To achieve this, the output frequency is a fraction of the fundamental crystal frequency (see notes below). The limitations to this type of design are the output frequency must be less than 24 MHz and most parts are not available with 100 ppm APR due to the higher frequency fundamental crystal. If 100 ppm APR is required, the phase noise will be slightly degraded.

* Fundamental crystal frequency is 32.768 MHz (32.768/2=16.384)

** Fundamental crystal frequency is 38.880 MHz (38.880/2=19.440)

Contact Application Engineering for any phase noise data on frequencies not listed.

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